

**METHOD AND APPARATUS FOR ENTERING A LOW POWER
MODE**

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Abstract of the Disclosure

A method and apparatus for entering a low power mode is provided. In one embodiment, data processing system (10) has power control circuitry (52) which may be used to control power usage in data processing system (10).

10 Power mode select circuitry (84) may be used to select a power mode.

Depending upon the power mode selected, power control circuitry (52) may use a cascaded approach to selecting which portions of data processing system (10) will be powered down, and thus how deeply data processing system (10) will be powered down.

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